that at least part of the information satisfies one or more conditions.

5(Amended). A circuit as claimed in claim 1, wherein said action is to prevent one or more modules from being granted access to the interconnect.

22(Amended). A functional circuit comprising:

an interconnect;

one or more modules connected to the interconnect; and

a monitoring circuit for monitoring information containing packets put onto the interconnect by one or more modules, said monitoring circuit comprising:

circuitry for determining if the information in a packet matches one or more conditions; and

circuitry for performing one or more actions if it is determined that information on the interconnect matches said one or more conditions.

23(Amended). A functional circuit as claimed in claim 22, wherein the functional circuit is an integrated circuit.

24(Amended). A functional circuit as claimed in claim 23, wherein at least one module is external to said integrated circuit.

25(Amended). A functional circuit as claimed in claim 22, wherein an arbiter is provided for arbitrating between the modules to determine which module is granted access to the interconnect at a given time.

26(Amended). A functional circuit as claimed in claim 25, wherein said determining circuitry is at least partially in the arbiter.

27(Amended). A functional circuit as claimed in claim 25, wherein said determining circuit does not delay the arbitration provided by the arbiter.

28(Amended). A functional circuit as claimed in claim 22, wherein said interconnect is a bus.

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29(Amended). A functional circuit as claimed in claim 22, wherein one of said modules comprises a debug module.

30(Amended). A functional circuit as claimed in claim 29, wherein at least some of said circuitry for performing at least one action is in said debug module.

31(Amended). A functional circuit as claimed in claim 25, wherein at least some of said circuitry for performing at least one action is in said arbiter.

32(Amended). A functional circuit as claimed in claim 29, wherein at least part of the determining circuitry is in the debug module.

33(Amended). A method comprising the steps of:
monitoring information containing packets on an interconnect, the information being put onto the interconnect by one or modules;
determining if the information on an interconnect satisfies one or more conditions; and
carrying out one or more actions if it is determined that the information containing packet satisfies one or more conditions.

34(Amended). A circuit for monitoring packet information on an interconnect, said information being put onto the interconnect by one or more modules connected to the interconnect, said circuit being arranged to determine if the information satisfies one or more conditions.

35(Amended). A circuit for monitoring packet information on an interconnect, said information being put onto the interconnect by one or more modules connected to the interconnect, said circuit being arranged to determine if the information satisfies one or more conditions and to select the information satisfying the one or more conditions.

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